

IN THE CLAIMS:

Please amend the claims as shown below. The claims, as pending in the subject application, read as follows:

1. (Currently Amended) An information processing apparatus comprising:
an interface arranged to connect to a serial bus compatible to or complying with the IEEE 1394 standard; and

a memory arranged to store, in a predetermined first address area, configuration ROM information complying with the IEEE 1212 standard, and to store configuration information identical to the configuration ROM information in a second address area different from the predetermined first address area,

wherein information stored in the predetermined first address area can be read out by a first type of reading operation but cannot be read out by a second type of reading operation, information stored in the second address area can be read out by the second type of reading operation, and a readable data size of the second type of reading operation is larger than that of the first type of reading operation.

2. (Previously Presented) The apparatus according to claim 1, wherein the configuration ROM information contains information used to refer to the second address area where the configuration information is stored.

3. (Currently Amended) The apparatus according to claim 1, wherein the configuration ROM information has a general format defined in the IEEE 1212 standard, and the a first entry in the a root directory of the configuration ROM information contains

information used to refer to the second address area where the configuration information is stored.

4. (Previously Presented) The apparatus according to claim 1, wherein the second address area where the configuration information is to be stored is an area where a block read transaction with a large block size is supported.

5. (Currently Amended) An information processing apparatus comprising:
an interface arranged to connect to a serial bus compatible to or complying with the IEEE 1394 standard; and

a memory arranged to store, in a predetermined first address area, configuration ROM information with a minimal format complying with the IEEE 1212 standard, and to store configuration information corresponding to the configuration ROM information with a general format complying with the IEEE 1212 standard in a second address area different from the predetermined first address area,

wherein information stored in the predetermined first address area can be read out by a first type of reading operation but cannot be read out by a second type of reading operation, information stored in the second address area can be read out by the second type of reading operation, and a readable data size of the second type of reading operation is larger than that of the first type of reading operation.

6. (Previously Presented) The apparatus according to claim 5, wherein the configuration ROM information with the minimal format contains information used to refer to the second address area where the configuration information is stored.

7. (Previously Presented) The apparatus according to claim 5, wherein the second address area where the configuration information is to be stored is an area where a block read transaction with a large block size is supported.

8. (Currently Amended) An information processing method of processing information between a plurality of devices connected to a serial bus compatible to or complying with the IEEE 1394 standard, comprising the steps of:

reading out at least part of configuration ROM information complying with the IEEE 1212 standard, which is stored in a predetermined first address area of a device; and

reading out configuration information identical to the configuration ROM information, which is stored in a second address area different from the predetermined first address area, on the basis of the readout information.

wherein information stored in the predetermined first address area can be read out by a first type of reading operation but cannot be read out by a second type of reading operation, information stored in the second address area can be read out by the second type of reading operation, and a readable data size of the second type of reading operation is larger than that of the first type of reading operation.

9. (Previously Presented) The method according to claim 8, wherein the configuration ROM information contains information used to refer to the second address area where the configuration information is stored.

10. (Currently Amended) The method according to claim 8, wherein the configuration ROM information has a general format defined in the IEEE 1212 standard, and ~~the~~ a first entry in ~~the~~ a root directory of the configuration ROM information contains information used to refer to the second address area where the configuration information is stored.

11. (Previously Presented) The method according to claim 8, wherein the second address area where the configuration information is to be stored is an area where a block read transaction with a large block size is supported.

12. (Currently Amended) An information processing method of processing information between a plurality of devices connected to a serial bus compatible to or complying with the IEEE 1394 standard, comprising the steps of:

reading out at least part of configuration ROM information with a minimal format complying with the IEEE 1212 standard, which is stored in a predetermined first address area of a device; and

reading out configuration information corresponding to the configuration ROM information with a general format according to the IEEE 1212 format, which is stored in a second address area different from the predetermined first address area, on the basis of the readout information,

wherein information stored in the predetermined first address area can be read out by a first type of reading operation but cannot be read out by a second type of reading operation, information stored in the second address area can be read out by the

second type of reading operation, and a readable data size of the second type of reading operation is larger than that of the first type of reading operation.

13. (Previously Presented) The method according to claim 12, wherein the configuration ROM information contains information used to refer to the second address area where the configuration information is stored.

14. (Previously Presented) The method according to claim 12, wherein the second address area where the configuration information is to be stored is an area where a block read transaction with a large block size is supported.

15. (Currently Amended) A computer program product stored on ~~comprising~~ a computer readable medium comprising ~~storing~~ a computer program code, for an information processing method of processing information between a plurality of devices connected to a serial bus compatible to or complying with the IEEE 1394 standard, comprising process procedure code for:

reading out at least part of configuration ROM information complying with the IEEE 1212 standard, which is stored in a predetermined first address area of a device; and

reading out configuration information identical to the configuration ROM information, which is stored in a second address area different from the predetermined first address area, on the basis of the readout information,

wherein information stored in the predetermined first address area can be read out by a first type of reading operation but cannot be read out by a second type of

reading operation, information stored in the second address area can be read out by the second type of reading operation, and a readable data size of the second type of reading operation is larger than that of the first type of reading operation.

16. (Currently Amended) A computer program product stored on comprising a computer readable medium comprising storing a computer program code, for an information processing method of processing information between a plurality of devices connected to a serial bus compatible to or complying with the IEEE 1394 standard, comprising process procedure code for:

reading out at least part of configuration ROM information with a minimal format complying with the IEEE 1212 standard, which is stored in a predetermined first address area of a device; and

reading out configuration information corresponding to the configuration ROM information with a general format complying with the IEEE 1212 format, which is stored in a second address area different from the predetermined first address area, on the basis of the readout information,

whercin information stored in the predetermined first address area can be read out by a first type of reading operation but cannot be read out by a second type of reading operation, information stored in the second address area can be read out by the second type of reading operation, and a readable data size of the second type of reading operation is larger than that of the first type of reading operation.